

1—30 ☐☐☐

Diagram of a 32-bit register structure:

- Field 1: 21–30 (10 bits)
- Field 2: PQ (2 bits)
- Field 3: MISS (1 bit)
- Field 4: 18 empty bits

30 → 30 →
 "2026-08-29"

A large rectangle is divided into 100 small squares. The top row of 10 squares is shaded blue, representing 10% of the total area.

10 **×** **16**590 EXP 30 30

11 **x40**610 EXP 30 30

12 **x65**645 EXP 30 50

13 ☐ ☐ ☐ **x15**840 EXP ☐ ☐ ☐ ☐ ☐ 50 ☐ ☐ ☐ 10

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